

Year 1	
Term 1	<b>ELEC9731</b> Robust and Linear Control Syst
	<b>Disciplinary Knowledge Elective</b>
	<b>Disciplinary Knowledge Elective</b>
Term 2	<b>ELEC3114</b> Control Systems
	<b>Disciplinary Knowledge Elective</b>
Term 3	<b>ELEC4632</b> Computer Control Systems
	<b>ELEC9732</b> Analysis and Design of Non-lin
	<b>ELEC3104*</b> Digital Signal Processing

Year 2	
Term 1	<b>ELEC9771</b> Project Report A
	<b>ELEC4633</b> Real-Time Engineering
	<b>Advanced Disciplinary Knowledge Elective</b>
Term 2	<b>ELEC9772</b> Project Report B
	<b>ELEC4631</b> Cont - Time Control Sys Design
Term 3	<b>GSOE9010 OR GSOE9011</b> Engineering Postgraduate Coursework Research Skills
	<b>Engineering Technical Management</b>
	<b>Advanced Disciplinary Knowledge Elective</b>

**NOTES**

This is intended as a guide only. Courses do not need to be studied in the exact structure that they appear here. Please see the handbook for details regarding each specialisation, its structure and subject term offerings. You can find your program requirements in the [UNSW Handbook](#), or alternatively your [Progression Checksheet](#) will give you an overview of your program.

\*Students can take ELEC3115 or ELEC3117 or ELEC3104 depending on term offerings.

Year 1	
Term 2	<b>ELEC3114</b> Control Systems
	<b>ELEC4631</b> Cont - Time Control Sys Design
Term 3	<b>ELEC4632</b> Computer Control Systems
	<b>ELEC9732</b> Analysis and Design of Non-lin
	<b>ELEC3104*</b> Digital Signal Processing
Term 1	<b>ELEC9731</b> Robust and Linear Control Syst
	<b>ELEC4633</b> Real-Time Engineering
	<b>Disciplinary Knowledge Elective</b>

Year 2	
Term 2	<b>ELEC9771</b> Project Report A
	<b>Disciplinary Knowledge Elective</b>
	<b>Disciplinary Knowledge Elective</b>
Term 3	<b>ELEC9772</b> Project Report B
	<b>Advanced Disciplinary Knowledge Elective</b>
Term 1	<b>GSOE9010 OR GSOE9011</b> Engineering Postgraduate Coursework Research Skills
	<b>Engineering Technical Management</b>
	<b>Advanced Disciplinary Knowledge Elective</b>

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Year 1	
Term 3	<b>ELEC9732</b> Analysis and Design of Non-lin
	<b>ELEC3104*</b> Digital Signal Processing
	<b>Disciplinary Knowledge Elective</b>
Term 1	<b>ELEC9731</b> Robust and Linear Control Syst
	<b>Disciplinary Knowledge Elective</b>
Term 2	<b>ELEC3114</b> Control Systems
	<b>Advanced Disciplinary Knowledge Elective</b>
	<b>Disciplinary Knowledge Elective</b>

Year 2	
Term 3	<b>ELEC9771</b> Project Report A
	<b>GSOE9010 OR GSOE9011</b> Engineering Postgraduate Coursework Research Skills
	<b>ELEC4632</b> Computer Control Systems
Term 1	<b>ELEC9772</b> Project Report B
	<b>ELEC4633</b> Real-Time Engineering
Term 2	<b>ELEC4631</b> Cont - Time Control Sys Design
	<b>Advanced Disciplinary Knowledge Elective</b>
	<b>Engineering Technical Management</b>

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Engineering

# Engineering Science (Masters)

24 UoC RPL / 48 UoC RPL



## 24 UoC of RPL

Year 1		Year 2	
Term 1	Engineering Course (6 UoC)	Term 1	Thesis C (4 UoC)
	Engineering Course (6 UoC)		Engineering Course (6 UoC)
	Engineering Course (6 UoC)		Engineering Course (6 UoC)
Term 2	Engineering Course (6 UoC)	Term 2	
	Engineering Course (6 UoC)		
	Thesis A (4 UoC or 6 UoC)		
Term 3	Thesis B (4 UoC or 6 UoC)	Term 3	
	Engineering Course (6 UoC)		
	Engineering Course (6 UoC)		

## 48 UoC of RPL

Year 1		Year 2	
Term 1	Thesis A (4 UoC or 6 UoC)	Term 1	
	Engineering Course (6 UoC)		
	Engineering Course (6 UoC)		
Term 2	Thesis B (4 UoC or 6 UoC)	Term 2	
	Engineering Course (6 UoC)		
	Engineering Course (6 UoC)		
Term 3	Thesis C (4 UoC)	Term 3	
	Engineering Course (6 UoC)		
	Engineering Course (6 UoC)		

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